

SSC8037GN4

P-Channel Enhancement Mode MOSFET

Features

V _{DS}	V_{GS}	R _{DS(ON)}	l _D
-30V	+20V	14mΩ@-10V	-25A
	<u> </u>	19mΩ@-4V5	-23/

> Description

This SSC8037GN4 uses advanced trench technology to provide excellent RDSON and low gate charge. The complementary MOSFETS may be used to form a level shifted high side switch, and for a host of other applications.

100% UIS + ΔVDS + Rg Tested!

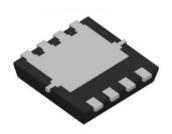
Applications

- Load Switch
- PWM Application
- Power Management

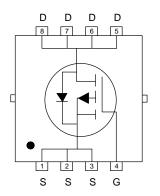
Ordering Information

Device	Package	Shipping
SSC8037GN4	PDFN3.3X3.3-8L	5000/Reel

Pin configuration



PDFN3.3X3.3-8L (Bottom View)



Pin Configuration (Top View)



Marking

(XXYY: Internal Traceability Code)



➤ Absolute Maximum Ratings (T_A=25°C unless otherwise noted)

Symbol	Parameter	Ratings	Unit		
V_{DSS}	Drain-to-Source Volta	Drain-to-Source Voltage		V	
V _{GSS}	Gate-to-Source Volta	Gate-to-Source Voltage		V	
L	Continuous Drain Current d	T _C =25℃	-25	Λ	
l _D	Continuous Drain Current	T _C =100°C	-17	Α	
,	Continuous Proin Current 3	T _A =25℃	-10	Δ.	
IDSM	Continuous Drain Current ^a	T _A =70°C	-7.5	Α	
I _{DM}	Pulsed Drain Current ^b		-100	Α	
Б	Down Discipation C	Tc=25℃	20.8	14/	
P _D	Power Dissipation °	T _C =100°C	8.3	W	
Б	Down Discipation 2	T _A =25℃	3.13	14/	
P _{DSM}	Power Dissipation ^a	T _A =70°C	2	W	
Eas	Avalanche Energy b L=0.5mH Single Pulse		42	mJ	
TJ	Operation junction temperature		-55~150	$^{\circ}$	
T _{STG}	Storage temperature ra	Storage temperature range			

➤ Thermal Resistance Ratings (T_A=25°C unless otherwise noted)

Symbol	Parameter	Ratings	Unit
$R_{\theta JA}$	Junction-to-Ambient Thermal Resistance a	40	°C AA/
R _{θJC}	Junction-to-Case Thermal Resistance	6	°C/W

Note:

- a. The value of R_{θJA} is measured with the device mounted on 1 in² FR-4 board with 2oz.copper, in a still air environment with T_A=25 °C. The value in any given application depends on the user is specific board design. The power dissipation is based on the t≤10s thermal resistance rating.
- b. Repetitive rating, pulse width limited by junction temperature.
- c. The power dissipation P_D is based on $T_{J(MAX)}$ =150°C, using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heat sinking is used.
- d. The maximum current rating is package limited.

SSC-V1.0 www.sscsemi.com Analog Future



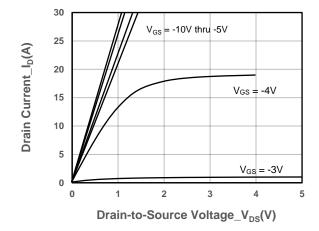


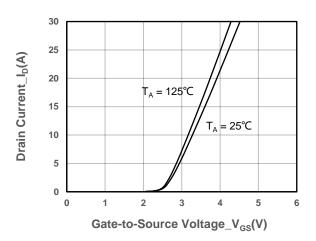
> Electrical Characteristics (T_A=25℃ unless otherwise noted)

Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} = 0V, I _D = -250μA	-30			V
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = -250uA$	-1	-1.6	-3	V
Drain Sauras On Basistanas	R _{DS(on)}	$V_{GS} = -10V, I_D = -7A$		14	19	m0
Drain-Source On-Resistance		V _{GS} = -4.5V, I _D = -5A		19	30	mΩ
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = -40V, V _{GS} = 0V			1	μA
Gate-Source Leak Current	lgss	$V_{GS} = \pm 20V$, $V_{DS} = 0V$			±100	nA
Transconductance	G _{FS}	V _{DS} = -5V, I _D = -1A		6.5		S
Forward Voltage	V _{SD}	V _{GS} = 0V, I _S = -5A		-0.75	-1.3	V
Gate Resistance	R _G	V _{DS} = 0V, f = 1MHz		11.5		Ω
Input Capacitance	Ciss	\/ 45\/\\ 0\/		1300		pF
Output Capacitance	Coss	V _{DS} = -15V, V _{GS} = 0V,		165		
Reverse Transfer Capacitance	Crss	f = 1MHz		182		
Total Gate Charge	Q _G	10)/)/ 45)/		25		
Gate to Source Charge	Q _{GS}	V _{GS} = -10V, V _{DS} = -15V,		4.3		nC
Gate to Drain Charge	Q _{GD}	I _D = -10A		6.1		
Turn-on Delay Time	T _{D(ON)}			8		
Rise Time	Tr	$V_{GS} = -10V$, $V_{DS} = -15V$,		33.5		
Turn-off Delay Time	T _{D(OFF)}	$R_L = 3\Omega$, $R_G = 1\Omega$		48		ns
Fall Time	Tf			11		
Diode Recovery Time	Trr	I _F =-10A, di/dt=200A/us		23		ns
Diode Recovery Charge	Q _{rr}	I _F =-10A, di/dt=200A/us		8		nC



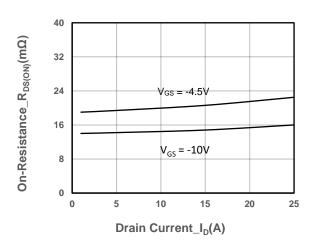
> Typical Performance Characteristics (T_A=25℃ unless otherwise noted)

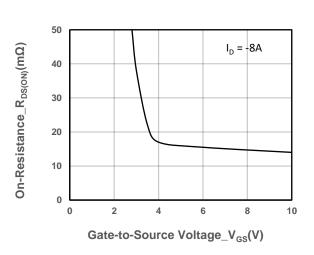




Output Characteristics

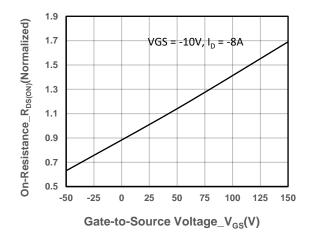
Transfer Characteristics

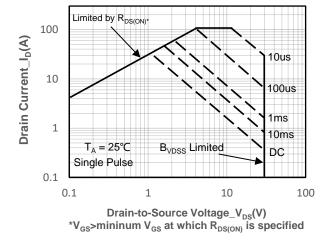




On-Resistance vs. Drain Current and Gate Voltag

On-Resistance vs. Gate-to-Source Voltage



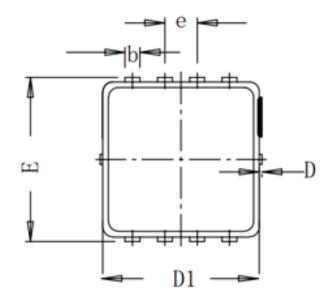


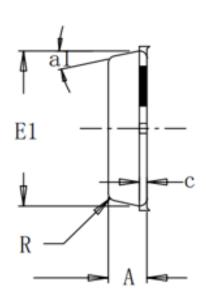
On-Resistance vs. Junction Temperature

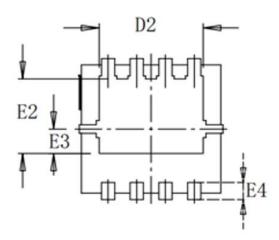
Safe Operating Area vs. Junction-to-Ambient



Package Information







Cumbal	Dimensions In Millimeters			
Symbol	Min.	Nom.	Max.	
Α	0.75	0.78	0.81	
b	0.297	0.3	0.35	
С	-	0.152	-	
D	0	0.05	0.1	
D1	3.12	3.15	3.18	
D2	-	2.35	-	
E	3.2	3.3	3.4	
E1	3.09	3.12	3.15	
E2	-	1.75	-	
E3	-	0.575		
E4	-	0.4	-	
R	-	0.15	-	
е	0.65BSC			
a1°	-	12°	-	



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